

8/11/04

10/066833

L Number	Hits	Search Text	DB	Time stamp
-	1903	Interrupt and pipeline and insert\$ and @py < "2002"	USPAT; US-PGPUB	2004/08/11 15:38
-	1405	Interrupt and pipeline and insert\$ and @py < "2001" and processor	USPAT; US-PGPUB	2004/08/11 15:38
-	251	Interrupt and pipeline and processor and (insert\$ near (instruction or opcode or code))	USPAT; US-PGPUB	2004/08/11 15:38
-	230	(insert\$ near (instruction or opcode or code)) and (pipeline near (instruction or opcode or code)) and processor	USPAT; US-PGPUB	2004/08/11 15:38
-	562	((pipeline processor microprocessor cpu) and (stack status flush "return address" push pop "save state")) and ((pipeline processor microprocessor cpu) and ((code state context function) near switch))) and ((pipeline processor microprocessor cpu) and ((interrupt function exception disrruption instruction) near call)))	USPAT; US-PGPUB	2004/08/11 16:02
-	11	((interrupt function exception disrruption instruction) near call)) near ((code state context function) near switch))	USPAT; US-PGPUB	2004/08/11 15:38
-	146	((interrupt function exception disrruption instruction) near call)) near handl\$	USPAT; US-PGPUB	2004/08/11 15:38
-	1226	("stack status" (pipeline near flush) "return address" "save state" "return state") and (pipeline processor microprocessor cpu)) and ((interrupt function exception disrruption instruction) near call))	USPAT; US-PGPUB	2004/08/11 15:39
-	479	((("stack status" (pipeline near flush) "return address" "save state" "return state") and (pipeline processor microprocessor cpu)) and ((interrupt function exception disrruption instruction) near call))) and handl\$) and pipeline	USPAT; US-PGPUB	2004/08/11 15:39
-	479	((interrupt function exception disrruption instruction) near call)) and ("stack status" (pipeline near flush) "return address" "save state" "return state") and handl\$ and pipeline	USPAT; US-PGPUB	2004/08/11 15:39
-	5	((((interrupt function exception disrruption instruction) near call)) near handl\$) and ("stack status" (pipeline near flush) "return address" "save state" "return state") and pipeline	USPAT; US-PGPUB	2004/08/11 15:39
-	1	("5867701").PN.	USPAT; US-PGPUB	2004/08/11 15:39
-	227	(712/219).CCLS.	USPAT; US-PGPUB	2004/08/11 15:39
-	1	high-performance and multi-processor and floating and point and unit	EPO	2004/08/11 15:39
-	2	"9918497"	USPAT; US-PGPUB; EPO; JPO; DERWENT;	2004/08/11 15:39
-	1	("5867701").PN.	IBM TDB USPAT; US-PGPUB	2004/08/11 15:39

-	33	("3911406" "4038533" "4050058" "4199811" "4342078" "4363091" "4376976" "4422144" "4542453" "4610000" "4654781" "4751703" "4875160" "4965724" "5003462" "5005118" "5101344" "5129067" "5134701" "5168571" "5202967" "5210758" "5212693" "5233694" "5235686" "5293592" "5307478" "5335344" "5371864" "5452426" "5463745" "5542059" "5546552").PN.	USPAT	2004/08/11 15:39
-	3	5867701.URPN.	USPAT	2004/08/11 15:39
-	338	instruction near (insert instertion)	USPAT; US-PGPUB	2004/08/11 15:39
-	75	(interrupt or function call or exception) near (embed or insert) and pipeline	USPAT; US-PGPUB	2004/08/11 15:39
-	1	5867701.pn.	USPAT	2004/08/11 15:39
-	276	instruction and pipeline	EPO	2004/08/11 15:40
-	1	01200425.5	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/11 15:40
-	1	EP000463966a2	EPO	2004/08/11 15:41
-	1	("5117498").PN.	USPAT; US-PGPUB	2004/08/11 15:41
-	1	("5155853").PN.	USPAT; US-PGPUB	2004/08/11 15:41
-	10	Interrupt and pipeline and insert\$	USPAT; US-PGPUB	2004/08/11 15:42
-	1680	Interrupt and pipeline and insert\$ and @py < "2001"	USPAT; US-PGPUB	2004/08/11 15:43
-	132	Interrupt and pipeline and @py < "2001" and processor and (insert\$ near (instruction or opcode or code))	USPAT; US-PGPUB	2004/08/11 15:43
-	242	(insert\$ near (instruction or opcode or code)) and (pipeline near (instruction or opcode or code))	USPAT; US-PGPUB	2004/08/11 15:43
1	2161	vliw or "very long instruction word"	USPAT; US-PGPUB	2004/08/11 15:56
-	2159	vliw or "very long instruction word" and processor	USPAT; US-PGPUB	2004/08/11 14:59
-	2098	(vliw or "very long instruction word") and processor	USPAT; US-PGPUB	2004/08/11 14:59
-	1046	(vliw or "very long instruction word") and processor and pipeline	USPAT; US-PGPUB	2004/08/11 15:00
-	940	(vliw or "very long instruction word") and processor and pipeline and parallel	USPAT; US-PGPUB	2004/08/11 15:01
-	210	(vliw or "very long instruction word") and processor and pipeline and parallel and insert	USPAT; US-PGPUB	2004/08/11 15:01
-	11	(vliw or "very long instruction word") and processor and pipeline and parallel and (insert near instruction)	USPAT; US-PGPUB	2004/08/11 15:02

Search history

7/29/04:

pipeline and interrupt

7/30/04:

interrupt and instruction and stack
interrupt and instruction and insert
exception handling
(exception or interrupt or function) and processor and pipelin

e

precise interrupts

8/11/04:

VLIW

VLIW and insert and pipeline